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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/829,668	04/22/2004	Douglas C. Burger	•	6831	
42640 DILLON & YU	7590 04/25/200 TDFLLLP	EXAM	EXAMINER		
8911 NORTH CAPITAL OF TEXAS HWY			FENNEMA	FENNEMA, ROBERT E	
	SUITE 2110 AUSTIN, TX 78759			PAPER NUMBER	
,,				•	
			MAIL DATE	DELIVERY MODE	
			04/25/2008	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

## Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)	
10/829,668	BURGER ET AL.	
Examiner	Art Unit	
ROBERT E. FENNEMA	2183	

	ROBERT E. FENNEMA	2183	
The MAILING DATE of this communication appe	ars on the cover sheet with the	correspondence add	ress
THE REPLY FILED 01 April 2008 FAILS TO PLACE THIS APP	LICATION IN CONDITION FOR A	LOWANCE.	
<ol> <li>M The reply was filed after a final rejection, but prior to or on application, applicant must timely file one of the following i application in condition for allowance, (2) a Notice of Appe for Continued Examination (RCE) in compliance with 37 C periods:</li> </ol>	replies: (1) an amendment, affidavi eal (with appeal fee) in compliance	t, or other evidence, w with 37 CFR 41.31; or	hich places the (3) a Request
<ul> <li>a) The period for reply expires 3 months from the mailing date</li> </ul>	of the final rejection.		
b) The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire Is Examiner Note: If box 1 is checked, check either box (a) or ( MONTHS OF THE FINAL REJECTION. See MPEP 706.07(	ater than SIX MONTHS from the mailing b). ONLY CHECK BOX (b) WHEN THE	date of the final rejection	n.
Extensions of time may be obtained under 37 CFR 1.136(a). The date have been filled is the date for purposes of determining the period of under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the set forth in (b) above, if checked. Any reply received by the Office later may reduce any earned patient term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL.	on which the petition under 37 CFR 1.1 ension and the corresponding amount hortened statutory period for reply origi than three months after the mailing dat	of the fee. The appropria nally set in the final Offic	ate extension fee e action; or (2) as
<ol> <li>The Notice of Appeal was filed on A brief in comp filing the Notice of Appeal (37 CFR 41.37(a)), or any exter Notice of Appeal has been filed, any reply must be filed with</li> </ol>	sion thereof (37 CFR 41.37(e)), to	avoid dismissal of the	
<u>AMENDMENTS</u>			
<ol> <li>The proposed amendment(s) filed after a final rejection, t</li> <li>(a) They raise new issues that would require further con</li> <li>(b) They raise the issue of new matter (see NOTE below</li> <li>(c) They are not deemed to place the application in better the contraction of the contraction</li></ol>	nsideration and/or search (see NO) w);	TE below);	
appeal; and/or (d)☐ They present additional claims without canceling a c	corresponding number of finally reje	ected claims.	
NOTE: (See 37 CFR 1.116 and 41.33(a)).			
4. The amendments are not in compliance with 37 CFR 1.12		mpliant Amendment (F	PTOL-324).
5. Applicant's reply has overcome the following rejection(s):			
Newly proposed or amended claim(s) would be all non-allowable claim(s).		•	
7. \( \bar{\text{\text{\$N\$}}} \) for purposes of appeal, the proposed amendment(s); a) \( \bar{\text{\$h\$}} \) how the new or amended claims would be rejected is prov The status of the claim(s) is (or will be) as follows: Claim(s) allowed:		l be entered and an ex	planation of
AFFIDAVIT OR OTHER EVIDENCE			
<ol> <li>The affidavit or other evidence filed after a final action, but because applicant failed to provide a showing of good and was not earlier presented. See 37 CFR 1.116(e).</li> </ol>	sufficient reasons why the affidavi	it or other evidence is	necessary and
<ol> <li>The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to o showing a good and sufficient reasons why it is necessary</li> </ol>	vercome <u>all</u> rejections under appea	al and/or appellant fails	s to provide a
10. The affidavit or other evidence is entered. An explanation REQUEST FOR RECONSIDERATION/OTHER	n of the status of the claims after er	ntry is below or attache	ed.
<ol> <li>The request for reconsideration has been considered but See Continuation Sheet.</li> </ol>	does NOT place the application in	condition for allowand	ce because:
12. Note the attached Information <i>Disclosure Statement</i> (s). (13. Other:	PTO/SB/08) Paper No(s)		
/Eddie P Chan/ Supervisory Patent Examiner, Art Unit 2183			

Continuation of 11, does NOT place the application in condition for allowance because: The applicant has argued that because a mechanism (a FIFO queue) is used to direct the results from one instruction to another instruction, that the results are not sent "directly" to another instruction. However, Examiner believes that this is far too literal of an interpretation to take of the word "directly". The Examiner takes the term "directly" in this context to mean that it is sent to the instruction, instead of being committed to the register file, and then having the receiving instruction receive that value from the register file. Examiner feels this is a reasonable interpretation of directly, because in pipelined machines, to bypass back to a previous stage, which would have to be done to make this work, there would necessarily be components between the result and the instruction, such as buffering/pipeline registers. If the Applicant truly wishes to claim a connection that has absolutely no components between the instruction result being formed and the other instructions input, then Examiner feels that that has to be explicitly claimed, because it is not what would be considered ordinary usage of the term "directly" in the context of the invention and the art. However, Examiner is not entirely sure there is support for the Applicant's usage of the term "directly" anyway. Referring to Figure 1, the output of the ALU is fed onto a bus, along with multiple other values, which are then connected into the stores. However, this very organization requires some kind of component or mechanism to arbitrate this bus, in the very least a multiplexor of some sort. Therefore, Examiner finds it very unlikely that the Applicant can provide support for their definition, as it would appear to be impossible to feed back the result to the instruction store without any kind of intervening component. Even if the drawings and specification do not explicitly mention any such component, it would appear to violate the basic nature of processor design to be able to perform such a feat (at least without it being expicitly explained how it was being done, because it is by no means common), and if that is the case, then the Applicant would have to explain and describe just how this is done before the Examiner can accept such a definition of "directly", without considering it new matter or an enablement/written description issue.